



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **OZAWA, Hiroyuki et al.**

Group Art Unit: 2815

Serial No.: 09/994,753

Corres. and Mail

Examiner: **CLARK, SHEILA V.**

Filed: November 28, 2001

**BOX AF**

P.T.O. Confirmation No.: 8648

For. **SEMICONDUCTOR INTEGRATED CIRCUIT WITH DUMMY PATTERNS**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: May 28, 2003

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated January 28, 2003 for one month from April 28, 2003 to May 28, 2003.

Attached please find a check in the amount of \$110.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

05/29/2003 KZEWIDIE 00000009 09994753.

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110.00 OP

Respectfully submitted,

**ARMSTRONG, WESTERMAN & HATTORI, LLP**

Thomas E. Brown  
Attorney for Applicant  
Reg. No. 44,450

TEB/kal  
Atty. Docket No. **011589**  
Suite 1000  
1725 K Street, N.W.  
Washington, D.C. 20006  
(202) 659-2930



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TECHNOLOGY CENTER 2800

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